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below.

Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z

~~Q. 5.6: A sequential circuit with two D flip flops A and B, two~~

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~~Solution~~  
~~inputs,  $x$  and  $y$ ; and one output  $z$~~   
~~is Q. 5.1: The D latch of Fig. 5.6 is~~  
~~constructed with four NAND gates~~  
~~and an inverter. Consider the Q.~~  
4.8: Design a code converter that  
converts a decimal digit from the  
8, 4, -2, -1 code to BCD Q. 5.4: A  
PN flip-flop has four operations:

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Solution, clear to 0, no change,  
complement, and set to 1, when  
Q. 5.8: Derive the state table and  
the state diagram of the  
sequential circuit shown in Fig.  
P5.8

---

Q. 4.26: Construct a 4-to-16-line  
decoder with five 2-to-4-line

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decoders with enable.

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Digital Logic Design - Unit 04 -  
Combinational Logic - Part 05 -  
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CompaBook M Morris Mano index  
*Q. 3.9: Find all the prime  
implicants for the following  
Boolean functions, and determine*

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*which are Q. 4.34: Implement the following Boolean function with a multiplexer (HDL—see Problem 4.48) What Is Digital Design? - Deepend*

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table/circuit diagram (using D-flip*

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decoder using 3x8 and 2x4

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~~Solution~~  
~~decoder Combinational circuit~~  
~~with decoder and external logic~~  
~~gates—Digital Electronics~~

~~Example Q. 3.11: Simplify the~~  
~~following Boolean functions: using~~  
~~five variable maps: (a)~~

~~$F(A,B,C,D,E) = \text{Sum Solutions}$~~   
~~Manual Digital Design With an~~

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*Solution*  
*Introduction to the Verilog HDL*  
*5th edition by Mano & Ciletti*  
*Q. 1.1: List the octal and*  
*hexadecimal numbers from 16 to*  
*32. Using A and B for the last two*  
*digits Q. 7.19: Tabulate the PLA*  
*programming table for the four*  
*Boolean functions listed below. Q.*

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~~3.15: Simplify the following  
Boolean function  $F$ , together with  
the don't care conditions  $d$ , and~~

---

Digital Design: Q. 1.13: Do the  
following conversion problems:

(a) Convert decimal 27.315 to  
binary

~~Design of Digital Circuits  
Lecture 5: Combinational Logic~~

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Morris Mano Chapter 8 Problems

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Mano, M. Morris, 1927– Digital design : with an introduction to the verilog hdl / M. Morris Mano, Michael D. Ciletti.—5th ed. p. cm.

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M. Morris Mano is an Emeritus Professor of Computer Engineering at the California State University, Los Angeles. His notable works include the Mano Machine, i.e. a theoretical

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